

CLOCK GENERATING METHOD AND APPARATUS FOR AN ASUNCHRONOUS TRANSMISSION

Clock generating method and apparatus for an asynchrouous transmission, wherein a plurality of actual receiving signal arrival times are detected and averaged, to thereby avoid influences of randomly distributed delay variations upon signal transmission. Clock synchronization between a transmitting clock and a receiving clock is recovered by correcting the receiving clock on the basis of the obtained average of the actual arrival times and an expected arrival time. Thereby, overflow and underflow of a receiver buffer can be prevented.